

11/15/01

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. **UTILITY** Patent Application

50	O.I.P.E.	PATENT DATE
SCANNED	AC4	Q.A. SS

APPLICATION NO. 09/987563	CONT/PRIOR F	CLASS 714	SUBCLASS 71	ART UNIT 2133	EXAMINER <i>V. [Signature]</i>
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APPLICANTS

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65.

TRAINING

TITLE

Multi-bit test circuit

PTO-2040
12/99

ISSUING CLASSIFICATION									
ORIGINAL		CROSS REFERENCE(S)							
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
INTERNATIONAL CLASSIFICATION									

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____				NOTICE OF ALLOWANCE MAILED	
				ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	

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